




Search Notes 	Application/Control No. 10092906	Applicant(s)/Patent Under Reexamination CHEN ET AL.
	Examiner Coughlan, Peter	Art Unit 2129

Notes	Date	Examiner
neural networks, frequency, cpu, clock speed, Chen, Tseng	07/12/2006	pdc
Chen, Tseng, neural network, clock speed/rate frequency, vector, radial, cpu,	09/02/2005	pdc
radial neural network, cpu, clock speed, rate, frequency, vector, change, execution control unit, variable frequency, register,	07/12/2006	pdc
changing clock speed, clock multiplier factor, neural, previous, first clock, finite, ide, dma, pci, gaussian, multiquadric, m basis functions,	09/29/2005	pdc
radial, agp, pci, gaussian, multiquadric	10/05/2005	pdc
dummy, skills, first, frequency, external, based, neural network, makagon, monza, clock speed, frequency, changing, multiplier, training, intelligent, ide, dma, agp, pci, itemset,	3/2-3/06	pdc
neural networks, frequency, cpu, speed, rate, changing, altering, modifyng, ide, agp, pci,	07/12/2006	pdc
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Class	SubClass	Date	Examiner
701	110	07/12/2006	pdg
713	501	07/12/2006	pdg
704	256	07/12/2006	pdg
713	322	07/12/2006	pdg
375	354	07/12/2006	pdg
700	1	07/12/2006	pdg
700	90	07/12/2006	pdg
706	1	07/12/2006	pdg
706	15	07/12/2006	pdg
706	45	07/12/2006	pdg

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Class	SubClass	Date	Examiner
706	19	07/17/2006	pdc
706	23	07/17/2006	pdc
706	39	07/17/2006	pdc
USPAT, USPG PUB			
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